

REFERENCE DESIGN INTERFACE BOARD VERSION 3.2

Release Date	Revision	Design Eng.	Design Note
02/23/2019	Rev. A	DN	Initial release
06/20/2019	Rev. B	DN	<ul style="list-style-type: none">- Fixed 2.5V pull around JP10.- Added bypass caps for level shifters.- Deleted J8 and connected JTAG signals to TP.
05/14/2020	Rev. C	CL	Added USB ESD protection Added LED for USB power indication Added 10-pin header J5 for J-Link support Added 10-pin header J4 for TC2050 Plug-of-Nail support Fixed VCC_BD 3V to 3.0V Fixed VCC_BUCK to only 2.5V Changed PWD / BBOOT level shifter to unidirectional part Replaced several headers with 0 ohm resistors
02/17/2022	Rev. D	Afan Lin	<ul style="list-style-type: none">- Deleted U7 and its components- Added VR2 and its components for VCCIO supply- Changed the net name VCC_BUCK to VCC_LDO- Modified VR1 circuit- Added options for PWD circuit

Atmosic Technologies, Inc

910 E. Hamilton Ave, Suite 550
Campbell, CA 95008, USA

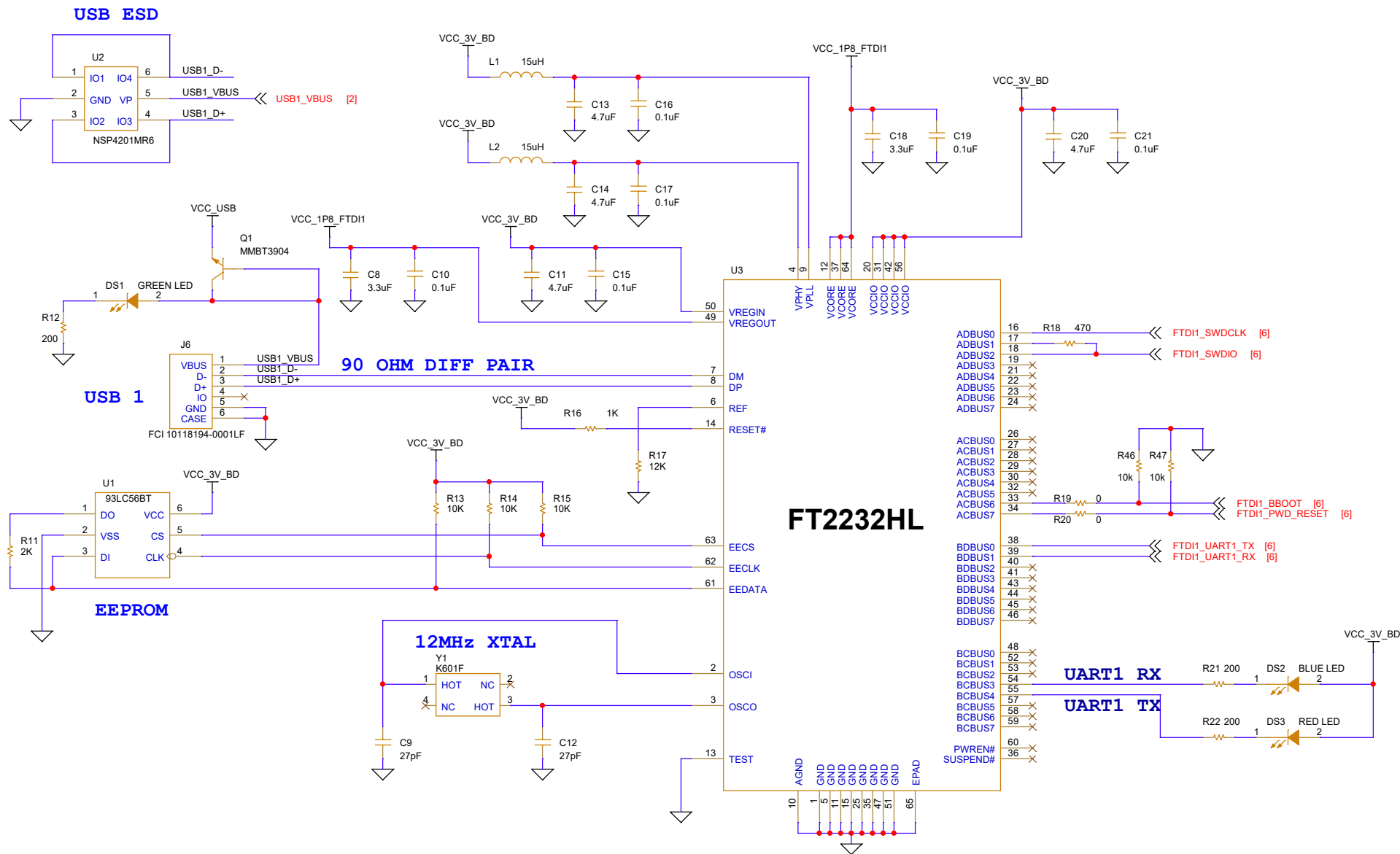
ATMOSIC TECHNOLOGIES INC

Title
REFERENCE DESIGN INTERFACE BOARD VERSION 3.2

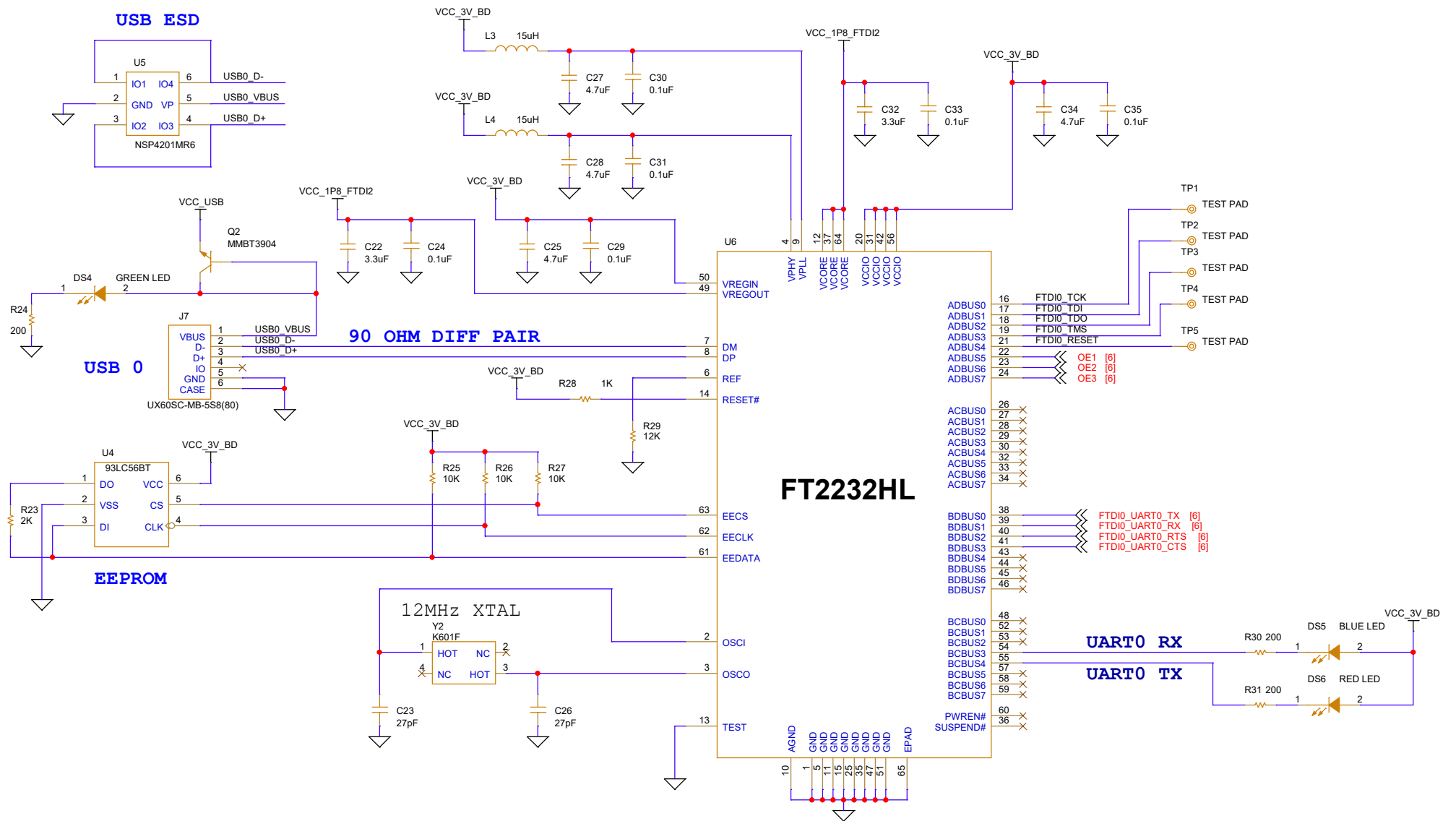
Size B	Document Number <Doc>	Rev D
-----------	--------------------------	----------

Date:	Friday, February 25, 2022	Sheet	1	of	6
-------	---------------------------	-------	---	----	---

Atmosic Technologies Proprietary and Confidential



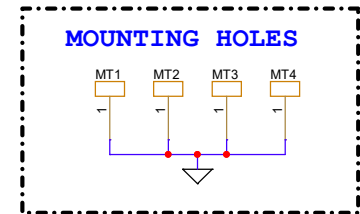
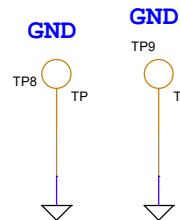
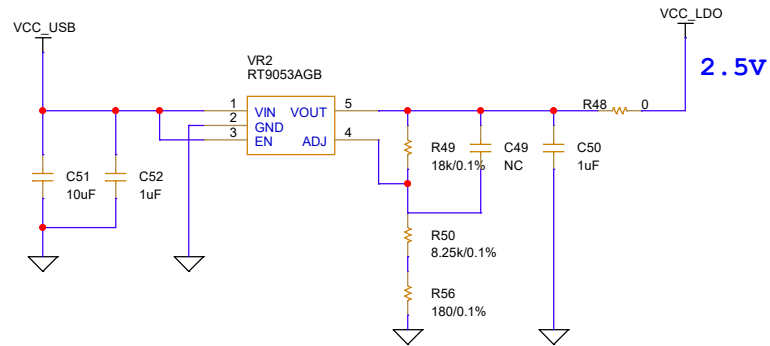
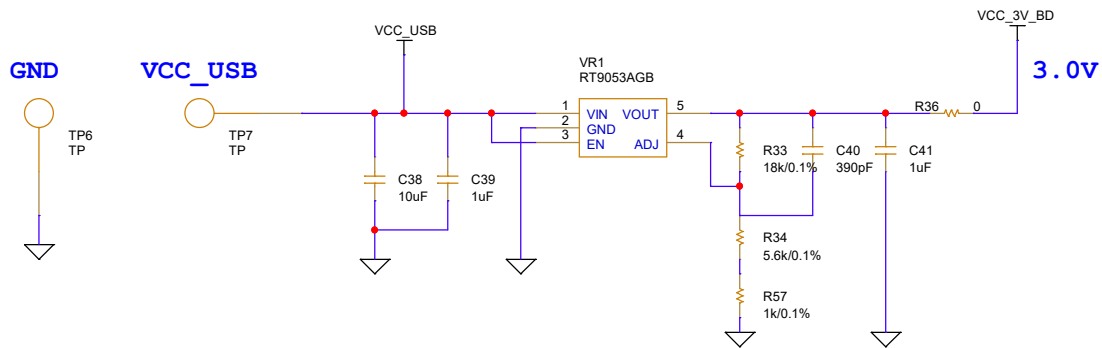
ATMOSIC TECHNOLOGIES INC		
Title		
REFERENCE DESIGN INTERFACE BOARD VERSION 3.2		
Size	Document Number	Rev
B	<Doc>	D
Date:	Friday, February 25, 2022	Sheet 3 of 6

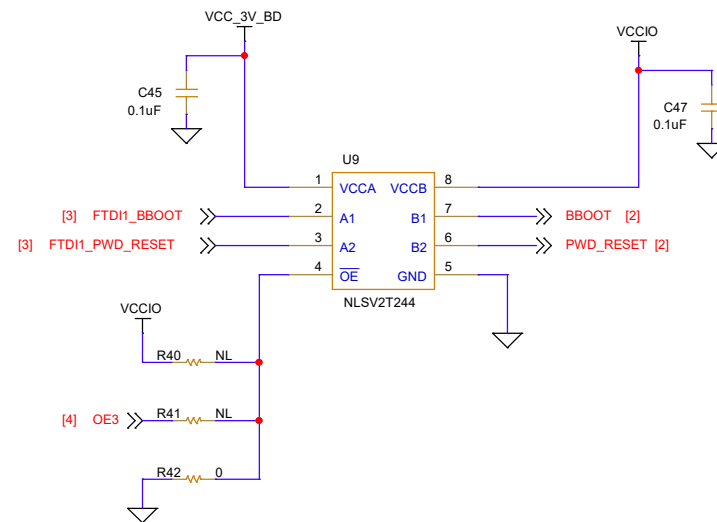
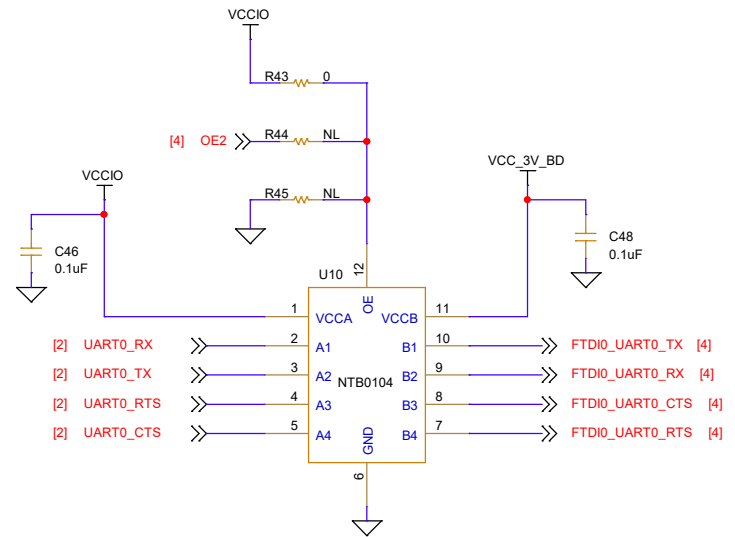
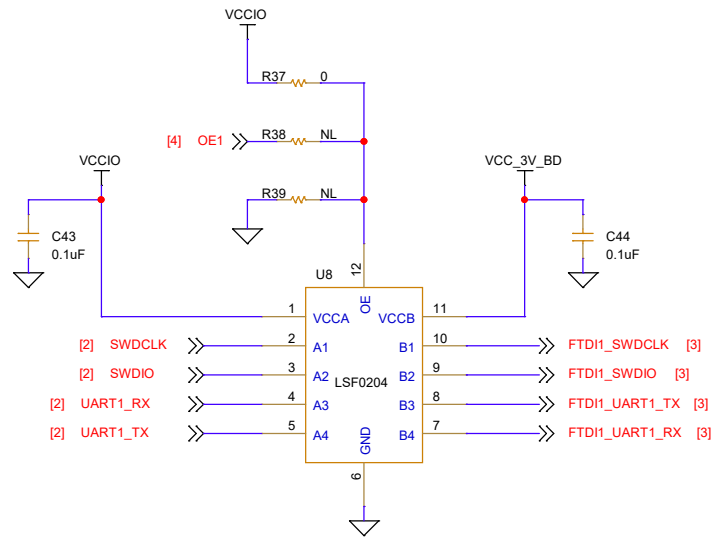


ATMOSIC TECHNOLOGIES INC			
Title			
REFERENCE DESIGN INTERFACE BOARD VERSION 3.2			
Size	Document Number		Rev
B	<Doc>		D
Date:	Friday, February 25, 2022		Sheet 4 of 6

Atmosic Technologies Proprietary and Confidential

Voltage Regulators





ATMOSIC TECHNOLOGIES INC			
Title		REFERENCE DESIGN INTERFACE BOARD VERSION 3.2	
Size B	Document Number <Doc>		Rev D
Date:	Friday, February 25, 2022	Sheet 6 of 6	