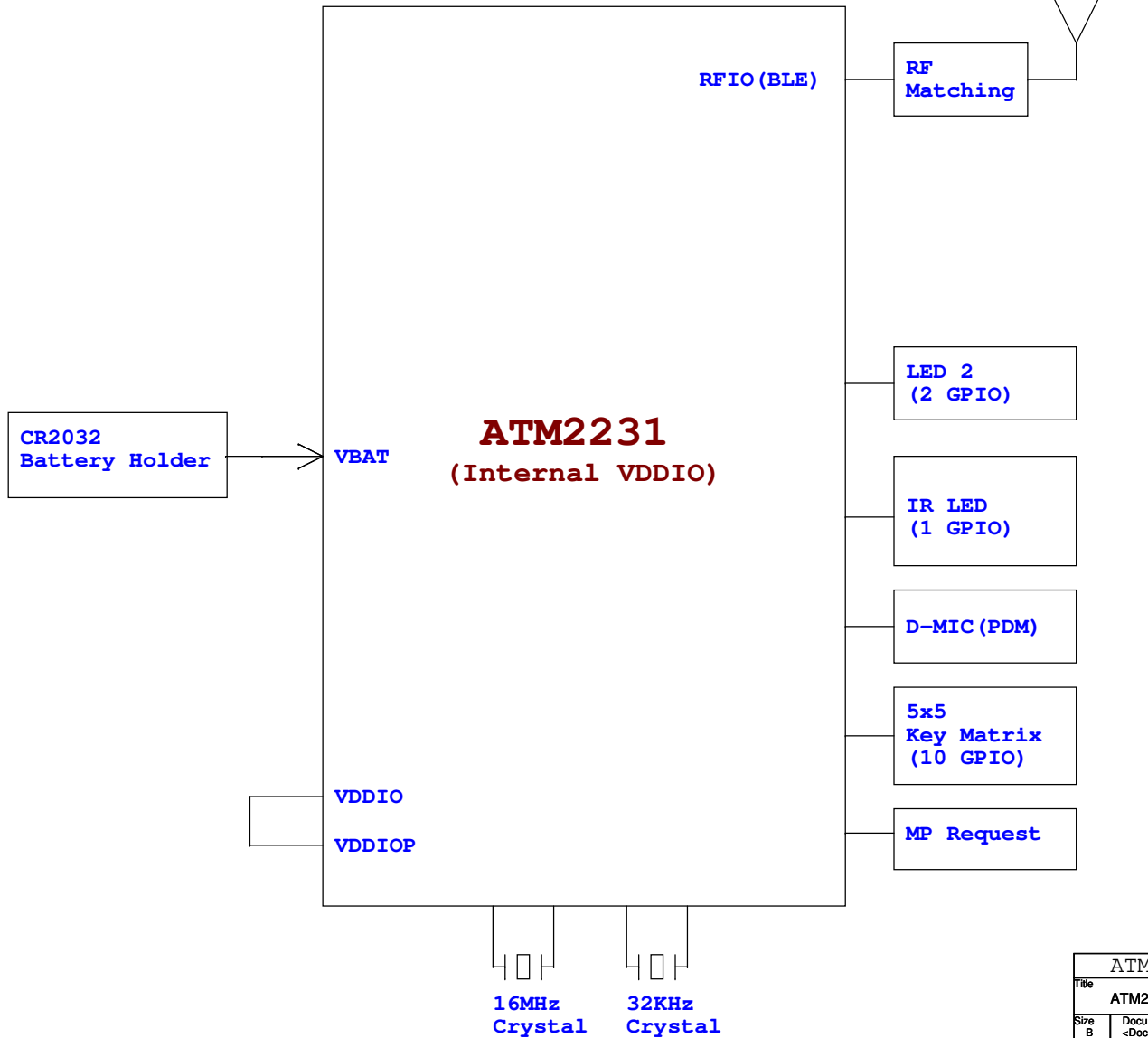


ATM2231_REMOTE_CONTROL_REF_DESIGN

2.4GHz
PCB Antenna



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[illegible]

VSTORE

DM8 DNS

DM10 DNS

HYBRID

C87 22uF/DNS

C84 10uF/DNS

C85 2.2uF/DNS

C88 1.0uF/DNS

C86 0.33uF/DNS

BAT54S-7-F/DNS

BOOST_IO

VOUT = 2.5V

Pin configuration diagram for the ATmega328P microcontroller. The diagram shows a top view of the chip with pins numbered 1 through 18. Pin 1 is SWDIO, Pin 2 is SWCLK, Pin 3 is R1/UART0_RX, Pin 4 is R4/UART0_RTS, Pin 5 is R2/UART0_CTS, Pin 6 is BB00T/LED-R, Pin 7 is PWD, Pin 8 is UART1_TX, Pin 9 is USB_5V, Pin 10 is LDO_OUT, Pin 11 is HYBRID, Pin 12 is VSTORE, Pin 13 is VDDIO, Pin 14 is VCCIO, Pin 15 is VBAT, and Pin 16 is VCC. Pins 17 and 18 are not connected.

RFIO HARV1_INP HARV1_INN RFIO HARV1_INP HARV1_INN

TP18 C0
 TP19 C1
 TP20 C2
 TP21 C3
 TP22 C4

RT0_TX R0
 RT0_RX R1
 RT0_CTS R2
 RT0_RTS R4
 TP26 R3

FPC TEST CONNECT

USB_5V

R90 100k/DNS

C77 1uF/DNS

C78 0.1uF/DNS

U12 RT90S3AGB/DNS

1 VIN

2 VOUT

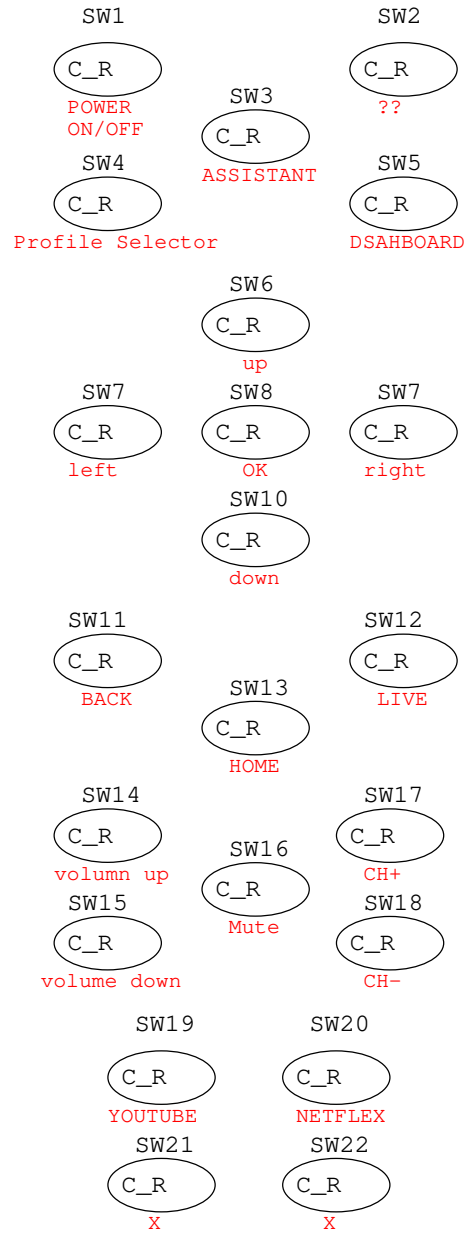
3 GND

4 EN

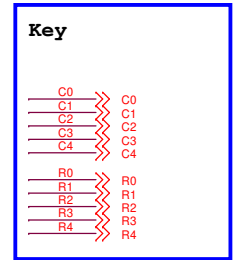
5 ADJ

$V_{OUT} = 0.6V \times ((R_A + R_B) / R_B)$
 $3.80V$ $R_A = 18k\Omega$, $R_B = 4.8k\Omega$

[illegible]



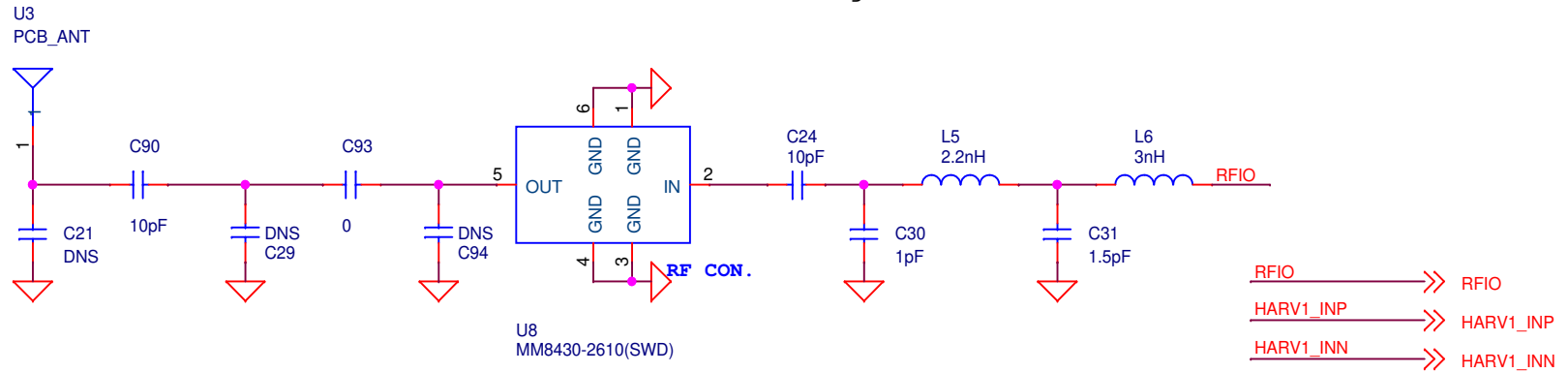
	Column 0 P29	Column 1 P28	Column 2 P12	Column 3 P9	Column 4 P8
Row 0 P23					
Row 1 P22					
Row 2 P21					
Row 3 P20					
Row 4 P13					



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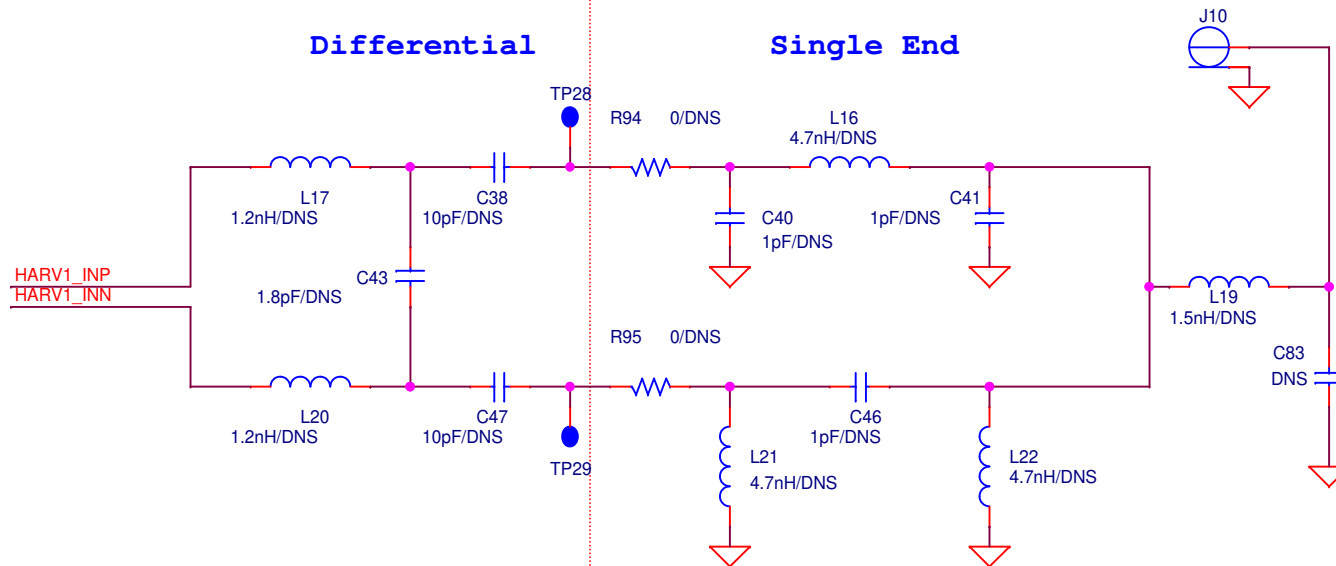
RFIO

BOM need to change



Differential

Single End



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