

Reference Design Programming Board

Release Date	Revision	Design Eng.	Design Note
12/30/2022	Rev. A	Chris TsaiLin	Initial release
02/19/2024	Rev. B	Chris TsaiLin	1. D2 and D9 added. 2. R33 and R49 change to 19.6K for compensation of diode voltage drop.

Atmosic Technologies, Inc
910 E. Hamilton Ave, Suite 550
Campbell, CA 95008, USA

ATMOSIC TECHNOLOGIES INC		
Title Reference Design Programming Board		
Size B	Document Number <Doc>	Rev B
Date:	Tuesday, March 05, 2024	Sheet 1 of 6

[illegible]

FROM REFERENCE DESIGN

The connector can be used when there is only test point (no FPC connector) on customer board.

VCCIO_RD

R233 0ohm

C2 0.1uF

VCC_3V_BD

R234 0ohm

C4 0.1uF

J1

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20

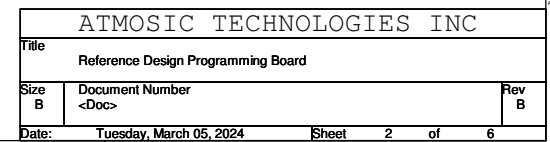
BBOOT_JLINK

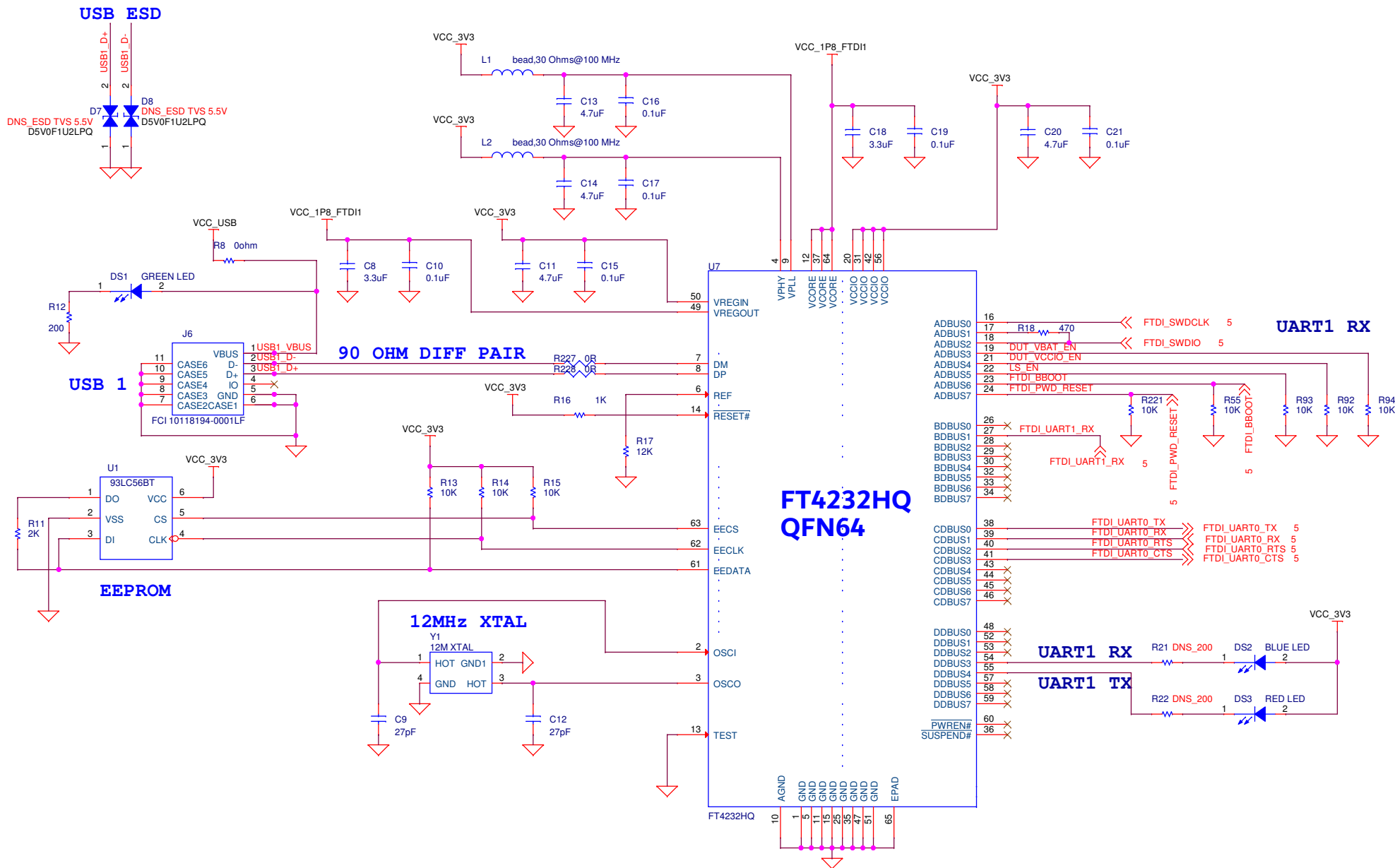
SWDIO_JLINK

SWDCLK_JLINK

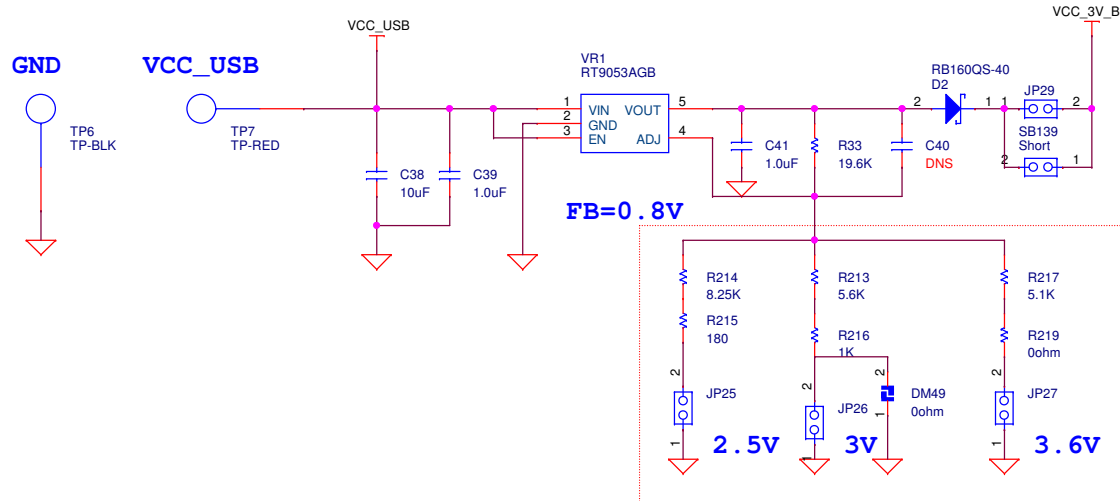
PWD_RESET_JLINK

HEADER 10X2 RA

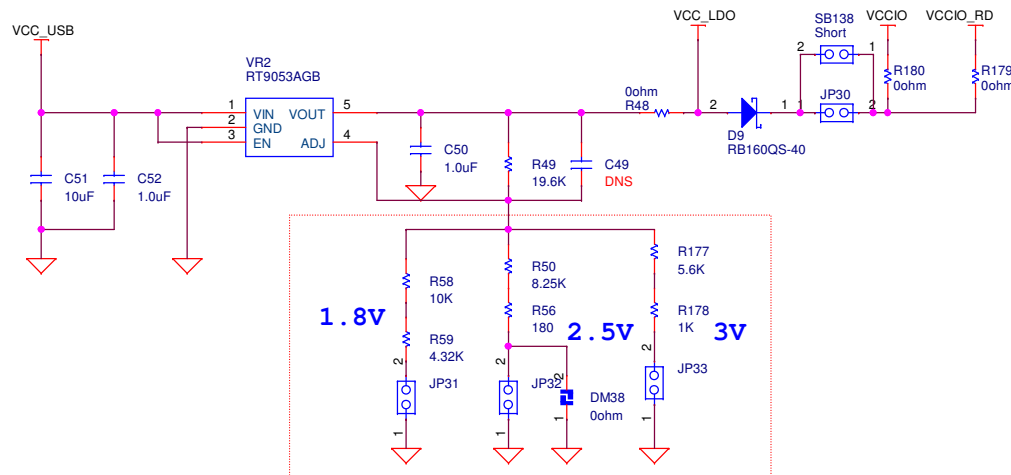




Voltage Regulators

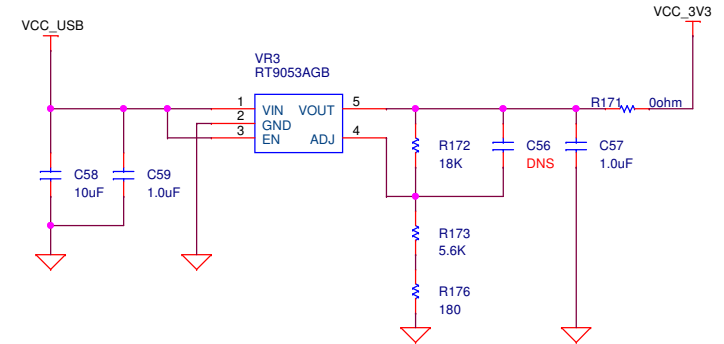


Default 3V to VBAT/VBATLI of Atmosic



Default 2.5V to VCCIO when external VDDIO

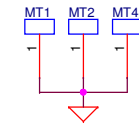
3.3V for FTDI-4232 chip



GND

TP8
TP-BLK

MOUNTING HOLES



ATMOSIC TECHNOLOGIES INC

Title		
Reference Design Programming Board		
Size	Document Number	Rev
B	<Doc>	B
Date:	Wednesday, March 06, 2024	Sheet 4 of 6

Atmosic Technologies Proprietary and Confidential

