

Analog/RF Design Engineering

[Atmosic Technologies](#) is looking for an Analog/RF Design Engineer who is passionate about delivering innovative low-power wireless connectivity solutions. Job title and responsibilities commensurate with experience.

Salary: \$120k - \$235k

Position: Full-time & in-person (San Jose, California)

Responsibilities:

- Design analog, RF, mixed-signal, data conversion, and power management circuits
- Circuit blocks may include low-noise amplifiers, power amplifiers, mixers, filters, data converters, switching regulators, LDOs, PLLs, frequency synthesizers, audio drivers
- Layout and modeling of circuit blocks
- Auto-calibration of circuit impairments
- Bench testing and performance tuning
- Work closely with cross-functional teams including algorithms, digital/SoC, ATE, and hardware systems during all phases of the product development cycle

Requirements:

- Good understanding of integrated radio and analog system tradeoffs
- Familiarity with modern CMOS technologies, integrated system-on-a-chip, and board-chip-antenna interfaces
- Strong analytical and debugging skills
- Self-driven with a can-do attitude
- Proven record of innovation and collaboration

Desired Experience:

- 3+ years of relevant experience in analog/RF circuit design, exceptional entry-level candidates will be considered
- MS in Electrical Engineering or related discipline required, PhD preferred

Location:

- US

Please email your resume to career@atmosic.com to apply.