Analog/RF Design Engineer

Atmosic Technologies is seeking self-motivated Analog/RF designers to join our team in developing cutting-edge, ultra-low-power wireless connectivity solutions for physical AI. This position offers a significant breadth of design responsibilities, spanning both baseband and RF integrated circuits.

Location: San Jose, CA (In-person, Full-time)
Salary Range: $145,000 – $235,000 (Commensurate with experience)

About Atmosic
Atmosic has designed and shipped the lowest-power bluetooth SoC in the world. Our innovative engineering solutions provide low-power connectivity and compute capabilities that enable extended battery life and battery-free operations. Join us in continuing to push the boundaries of power efficiency while developing new products to serve a growing range of applications in the physical AI era.

Key Responsibilities

- Our unique organization provides designers broad design experience across analog, RF, mixed-signal, data conversion, and power management domains. In this role, you will:
- Drive innovation in circuit design, layout, and validation.
- Apply creativity from the architecture to transistor-level circuits to push technology boundaries and deliver industry-leading, low-power solutions.
- Design critical circuit blocks, including VCOs, frequency synthesizers, LNAs, PAs, mixers, filters, ADCs, DACs, switching regulators, LDOs, and PLLs.
- Develop architectures involving digital-assisted design (calibration and trimming) to mitigate circuit non-idealities, process variations, and other impairments.
- Design for robust, best-in-class system-level performance that is ready for high-volume production.
- Collaborate closely with cross-functional teams throughout the entire product lifecycle, from concept and architecture to customer launch

Required Qualifications

- Deep understanding of transistor-level design.
- Strong analytical, problem-solving, and laboratory debugging skills.
- Self-motivated with a proven track record of cross-functional collaboration.

- Proficient with industry-standard EDA design tools (Cadence Virtuoso, Spectre/SpectreRF, Keysight ADS) and EM simulation software.
- PhD in Electrical Engineering with a focus on RF/Analog IC design, or MS with 3+ years of relevant industry experience

Desired Experience

- Experience in integrated transceiver architectures with complex RF/analog system trade-offs (noise, linearity, power, area)
- Technical Experience: Hands-on experience with advanced CMOS/FinFET process nodes and integrated SoC designs

To apply, please send your resume to career@atmosic.com.

See job postings at https://atmosic.com/about/#careers.